



LV99D Series 3.3 V LVDS Clock Oscillators

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- Pletronics' LV99D Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- The package is designed for high density surface mount designs.
- Low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Output frequency is synthesized.
- Low Jitter

**Pletronics Inc. certifies this device is in accordance with the
RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.**

Pletronics Inc. guarantees the device does not contain the following:
Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.16 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020C
Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to +5.0V
V _i Input Voltage	-0.5V to V _{CC} + 0.5V
V _o Output Voltage	-0.5V to V _{CC} + 0.5V

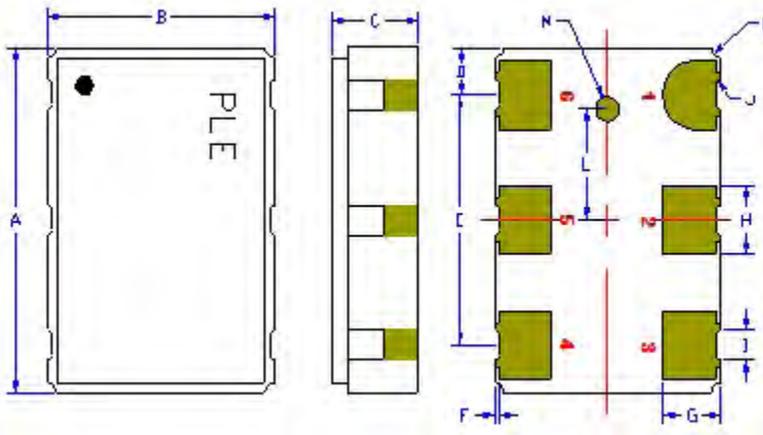
Thermal Characteristics

The maximum die or junction temperature is 155°C
The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

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Mechanical:



	Inches	mm
A	0.276 ±0.006	7.00 ±0.15
B	0.197 ±0.006	5.00 ±0.15
C	0.067 max	1.70 max
D ¹	0.038	0.96
E ¹	0.200	5.08
F ¹	0.004	0.10
G ¹	0.050	1.27
H ¹	0.055	1.40
I ¹	0.024	0.60
J ¹	0.004r	0.10r
K ¹	0.008r	0.20r
L ¹	0.089	2.25
M ¹	0.010r	0.25r

Contacts:

Gold 11.8 to 39.4 μinches (0.3 to 1.0 μm)
over
Nickel 50 to 350 μinches (1.27 to 8.89 μm)

Center metalized pad "M" on the base is
not internally connected.

¹ Typical dimensions

Not to Scale

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. If <0.80 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to V _{CC} if the oscillator is to be always on.
2	No connect	This pad should be connected to Ground or Supply Voltage to lower the packages thermal resistance.
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal termination. Capacitor coupled terminations can be used.
5	Output*	
6	Supply Voltage (V _{CC})	Recommend connecting appropriate power supply bypass capacitors as close as possible.